

Patent Abstracts of Japan

PUBLICATION NUMBER : 2002152017
 PUBLICATION DATE : 24-05-02

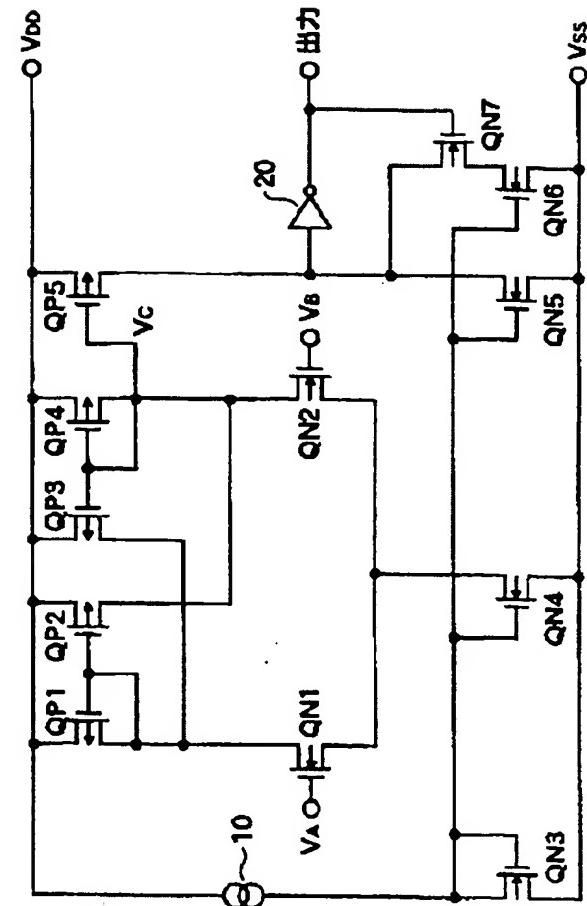
APPLICATION DATE : 07-11-00
 APPLICATION NUMBER : 2000339389

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INT.CL. : H03K 5/08

TITLE : HYSTERESIS COMPARISON CIRCUIT



ABSTRACT : PROBLEM TO BE SOLVED: To provide a hysteresis comparison circuit with a high input impedance that obtains a stable output even when a noise is intruded in an input signal.

SOLUTION: The hysteresis comparison circuit is provided with a differential pair including a first transistor(TR) QN1 and a second TR QN2, a third TR QP5 that amplifies a drain level of the first or second TR, an inverting amplifier means 20 that amplifies a drain level of the third TR, means QP1, QP2 that apply positive feedback to the drain of the second TR on the basis of the drain current of the first TR, means QP3, QP4 that apply positive feedback to the drain of the first TR on the basis of the drain current of the second TR, and a means QN7 that applies positive feedback to the drain of the third TR on the basis of an output of the inverting amplifier means.

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